

DS21T16 SCSI Terminator

FEATURES

- Fully compliant with SCSI-1, Fast SCSI and Ultra SCSI
- Pin compatible to the DS2107AE and DS21S07AE
- · Provides active termination for nine signal lines
- Supports TERMPWR 2.7V 5.50V
- · Low dropout voltage regulator
- Power-down mode isolates termination resistors from the bus
- SCSI bus hot-plug compatible
- · Fully supports actively negated SCSI signals
- Onboard thermal shutdown circuitry
- 20-pin plastic TSSOP (Thin Shrink Small Outline Package) (DS21T16E)

DESCRIPTION

Fast SCSI and Ultra SCSI require the use of active terminations at both ends of every cable segment in a SCSI system with single–ended drivers and receivers. The DS21T16 SCSI Terminator, which is fully compliant with these standards, enables the designer to gain the benefits of active termination: greater immunity to voltage drops on the TERMPWR (TERMination PoWeR) line, enhanced high–level noise immunity, intrinsic TERMPWR decoupling, and very low quiescent current consumption. The DS21T16 integrates a regulator and nine precise switched 110 ohms termination resistors into a monolithic IC. The DS21T16 can be electrically isolated from the SCSI bus without physical removal from the SCSI device.

FUNCTIONAL DESCRIPTION

The DS21T16 consists of a bandgap reference, buffer amplifier, and nine termination resistors (Figure 1). The bandgap reference circuit produces a precise 2.55V

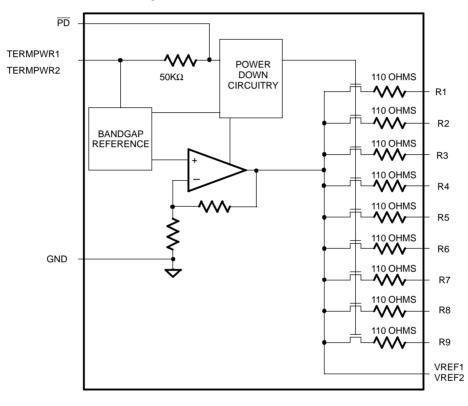
PIN ASSIGNMENT

| TERMPWR1 | | 1 | \bigcirc | 20 | | PD |
|---------------------------------|---|----|------------|----|--|----------|
| HS-GND | | 2 | | 19 | | VREF2 |
| R1 | | 3 | | 18 | | HS-GND |
| R2 | | 4 | | 17 | | NC |
| R3 | | 5 | | 16 | | R9 |
| R4 | ╓ | 6 | | 15 | | R8 |
| R5 | | 7 | | 14 | | R7 |
| HS-GND | | 8 | | 13 | | R6 |
| VREF1 | ╓ | 9 | | 12 | | HS-GND |
| GND | ᆂ | 10 | | 11 | | TERMPWR2 |
| DS21T16E 20-PIN TSSOP (173 MIL) | | | | | | |

level which is fed to a buffer amplifier. The buffer produces a 2.7V level and is capable of sourcing at least 24 mA into each of the termination resistors when the signal line is low (active). When the driver for a given signal line turns off, the terminator will pull the signal line to 2.7V (quiescent state). To handle actively negated SCSI signals, the buffer can sink at least 200 mA, and VRFF will move less than 60 mV. When all lines settle in the guiescent state, the regulator will consume about 2.5 mA. When the DS21T16 is put into power-down mode by bringing PD low, the power-down circuitry will turn off the transistors on each signal line. This will isolate the DS21T16 from the signal lines and effectively remove it from the circuit. The power-down pin (\overline{PD}) has an internal 50K Ω pull-up resistor. To place the DS21T16 into an active state, the PD pin should be left open circuited. When installed on disk drives or RAID system components, the DS21T16 will not affect the SCSI bus during a hot plug operation.

To ensure proper operation, both the TERMPWR1 and TERMPWR2 pins must be connected to the SCSI bus TERMPWR line and both the VREF1 and VREF2 pins must be tied together externally. Each DS21T16 requires a 4.7 μ F capacitor connected between the VREF pins and ground. Figure 2 details a typical SCSI bus configuration. In an 8–bit wide SCSI bus arrangement

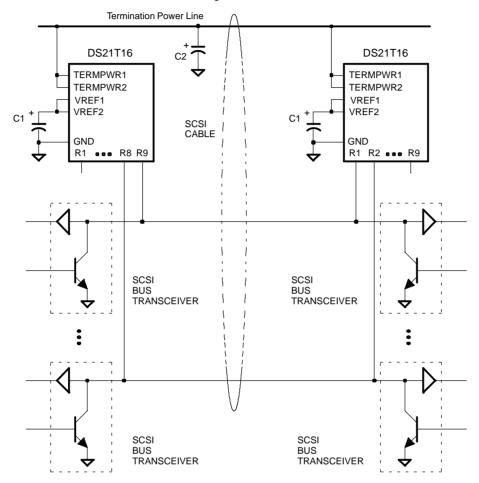
("A" Cable), two DS21T16s would be needed at each end of the SCSI cable in order to terminate the 18 active signal lines. In a 16-bit wide SCSI bus arrangement ("P" Cable), three DS21T16s would be needed at each end of the SCSI cable in order to terminate the 27 active signal lines.



DS21T16 BLOCK DIAGRAM Figure 1

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TYPICAL SCSI BUS CONFIGURATION Figure 2



NOTES:

- If the DS21T16 is to be embedded into a peripheral that will act as a target on a SCSI bus, it is that recommended TERMPWR be derived from the SCSI cable, not generated locally. In this configuration, if a power failure occurs in the peripheral, it will not affect the bus.
- 3. A high frequency bypass capacitor (0.1 μ F recommended) can be added in parallel to C1 for applications using fast rise/fall time drivers.

^{1.} C1 = 4.7 μ F tantalum C2 = 2.2 μ F tantalum or 4.7 μ F aluminum

DS21T16

PIN DESCRIPTION Table 1

| DS21T16E PIN | DS21T16S PIN | SYMBOL | DESCRIPTION |
|-----------------|-----------------|----------|--|
| 1 | 1 | TERMPWR1 | Termination Power 1. Should be connected to the SCSI TERMPWR line. Must be decoupled with either a 2.2 μF or 4.7 $\mu F.$ See Figure 2. |
| 3 | 2 | R1 | Signal Termination 1. 110 ohms termination. |
| 4 | 3 | R2 | Signal Termination 2. 110 ohms termination. |
| 5 | 4 | R3 | Signal Termination 3. 110 ohms termination. |
| 6 | 5 | R4 | Signal Termination 4. 110 ohms termination. |
| 7 | 6 | R5 | Signal Termination 5. 110 ohms termination. |
| 9 | 7 | VREF1 | Reference Voltage 1. Must be externally connected directly to the VREF2 pin. Must be decoupled with a 4.7 μ F capacitor as shown in Figure 2. |
| 10 | 8 | GND | Ground. Signal ground; 0.0V. |
| 11 | 9 | TERMPWR2 | Termination Power 2. Should be connected to the SCSI TERMPWR line. Must be decoupled with either a 2.2 μF or 4.7 $\mu F.$ See Figure 2. |
| 13 | 10 | R6 | Signal Termination 6. 110 ohms termination. |
| 14 | 11 | R7 | Signal Termination 7. 110 ohms termination. |
| 15 | 12 | R8 | Signal Termination 8. 110 ohms termination. |
| 16 | 13 | R9 | Signal Termination 9. 110 ohms termination. |
| 17 | 14 | NC | No Connect. Do not connect any signal to this pin. |
| 19 | 15 | VREF2 | Reference Voltage 2. Must be externally connected directly to the VREF1 pin. Must be decoupled with a 4.7 μ F capacitor as shown in Figure 2. |
| 20 | 16 | PD | Power Down. When tied low, the DS21T16 enters a power–down mode. Contains an internal 50K pull–up. Strap low to deactivate the DS21T16, leave open circuited to activate the DS21T16. |
| 2, 8, 12, 18 | N/A | HS-GND | Heat Sink Ground. Internally connected to the mounting pad. Should be either grounded or electrically isolated from other circuitry. |

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ABSOLUTE MAXIMUM RATINGS*

Voltage on Any Pin Relative to Ground Operating Temperature Storage Temperature Soldering Temperature -1.0V to +6.0V 0°C to 70°C -55°C to +125°C 260°C for 10 seconds

* This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

The Dallas Semiconductor DS21T16 is built to the highest quality standards and manufactured for long term reliability. All Dallas Semiconductor devices are made using the same quality materials and manufacturing methods. However, the DS21T16 is not exposed to environmental stresses, such as burn–in, that some industrial applications require. For specific reliability information on this product, please contact the factory in Dallas at (972) 371–4448.

| RECOMMENDED OPERATING CONDITIONS | | | | | (0°C to 70°C) | |
|----------------------------------|------------------|------|-----|----------------------|---------------|-------|
| PARAMETER | SYMBOL | MIN | ТҮР | MAX | UNITS | NOTES |
| TERMPWR Voltage | V _{TP} | 2.7 | 3.3 | 5.50 | V | |
| PD Active | V _{PDA} | -0.3 | | +0.8 | V | |
| PD Inactive | V _{PDI} | 2.0 | | V _{TP} +0.3 | V | |

| DC CHARACTERISTICS | | - | | | ((| 0°C to 70°C |
|---------------------------------------|------------------------------------|-------|-----|----------|----------|--------------|
| PARAMETER | SYMBOL | MIN | ТҮР | MAX | UNITS | NOTES |
| TERMPWR Current | I _{TP} I _{TP} | | 2.5 | 250 5 | mA mA | 1, 3 1, 4 |
| Power Down Current | I _{PD} | | 100 | 150 | μA | 1, 2, 5 |
| Termination Resistance | R _{TERM} | 107.5 | 110 | 119.5 | ohms | 1, 2 |
| Die Thermal Shutdown | T _{SD} | 150 | | | °C | 1, 6 |
| Power Down Termination Capacitance | C _{PD} | | 3.0 | 5.0 | pF | 1, 2, 5, 6 |
| Input Leakage High | IIH | -1.0 | | | μΑ | 1, 8 |
| Input Leakage Low | IIL | | | 1.0 | μΑ | 1, 7 |

REGULATOR CHARACTERISTICS

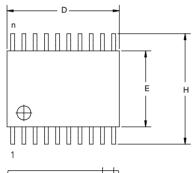
| PARAMETER | SYMBOL | MIN | ТҮР | MAX | UNITS | NOTES |
|------------------|-------------------|-----|-----|-----|-------|-------|
| Output Voltage | V _{REF} | 2.5 | 2.7 | 3.0 | V | 1, 2 |
| Drop Out Voltage | V _{DROP} | | 0.1 | 0.2 | V | 3, 6 |
| Line Regulation | LI _{REG} | | 1.0 | 2.0 | % | 1, 4 |
| Load Regulation | LO _{REG} | | 1.3 | 3.0 | % | 1, 3 |
| Current Limit | I _{LIM} | | 950 | | mA | 1 |
| Sink Current | I _{SINK} | 200 | | | mA | 1 |

(0°C to 70°C)

NOTES:

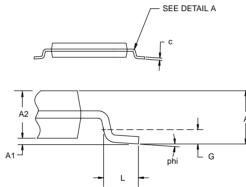
- 1. 2.7V < TERMPWR < 3.3V.
- 2. $0.0V \leq signal lines \leq TERMPWR$.
- 3. All signal lines = 0.0V.
- 4. All signal lines open.
- 5. <u>PD</u> = 0.0V.
- 6. Guaranteed by design; not production tested.
- 7. R_1 through R_9 only.
- 8. R_1 through R_9 and \overline{PD} .

DS21T16E SCSI TERMINATOR 20-PIN TSSOP





| DIM | MIN | MAX | | | |
|-------|----------|------|--|--|--|
| A MM | - | 1.10 | | | |
| A1 MM | 0.05 | - | | | |
| A2 MM | 0.75 | 1.05 | | | |
| СММ | 0.09 | 0.18 | | | |
| LMM | 0.50 | 0.70 | | | |
| e1 MM | 0.65 BSC | | | | |
| BMM | 0.18 | 0.30 | | | |
| D MM | 6.40 | 6.90 | | | |
| EMM | 4.40 NOM | | | | |
| G MM | 0.25 REF | | | | |
| н мм | 6.25 | 6.55 | | | |
| phi | 0° | 8° | | | |





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